

Abstract of the Disclosure

A polyphase channelizer converts an intermediate frequency wideband signal into a complex signal that is sampled by parallel analog-to-digital converters having a bank of samplers respectively clocked by staggered clocking signals for respective converters for feeding I and Q quadrature samples to a polyphase filter bank of finite impulse response filters driving a fast Fourier transform processor for providing channelized digital signal outputs. The parallel analog-to-digital converters can operate at lower speeds but are parallel connected for effectively operating at required higher speeds. The channelizer channelizes the wideband signal using a polyphase clock for enabling high speed sampling and converting through low speed parallel analog to digital converters.

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